



4:17□ 19□ 9:2

[illegible]

143:2

[illegible][illegible]

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[illegible][illegible][illegible]

10:3-4 □

[illegible]

9:30

[illegible][illegible][illegible]

□□□□□ 3:21-26□□□□□ 5:18-19□

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Diagram illustrating a 16-bit bus structure. The bus is divided into four segments, each containing 16 bits, for a total of 64 bits. The segments are labeled 1, 2, 3, and 4, with the label '1:16' indicating the range of the first segment.

3:16 1:19



 10
 5:21

53:6□

Diagram illustrating the relative lengths of five horizontal bars, labeled with time values on the right:

- Bar 1: 9:11 18
- Bar 2: 3:30
- Bar 3: (unlabeled)
- Bar 4: (unlabeled)
- Bar 5: (unlabeled)

11:25-26□□

21

11:18-

[illegible]

3:16

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